

# PATENT ABSTRACTS OF JAPAN

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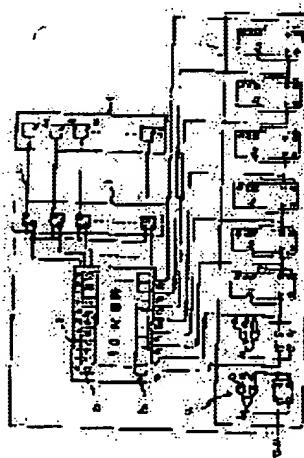
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## (54) MICROCOMPUTER

### (57)Abstract:

**PURPOSE:** To present the microcomputer which can reduce the chip area of the microcomputer and can obtain plural arbitrary phase pulses having various cycles.

**CONSTITUTION:** Output pulse levels are stored in bit trains 2a-2n corresponding to the respective addresses of a storage circuit 2 having the outputs of a timer circuit 1, which defines a clock as a count source, as the addresses and the respective bit outputs of this storage circuit 2 are latched synchronously to the above-mentioned clock and outputted to a port output circuit 4.



## LEGAL STATUS

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